

Overview

The patented **AlSensis**® A0061 Touch IC is a complete BASIC 1 – 15 input touch sensing solution. It includes all signal processing functions necessary to provide robust sensing under a wide variety of changing conditions. Only minimal, low cost components are required for standard operation.

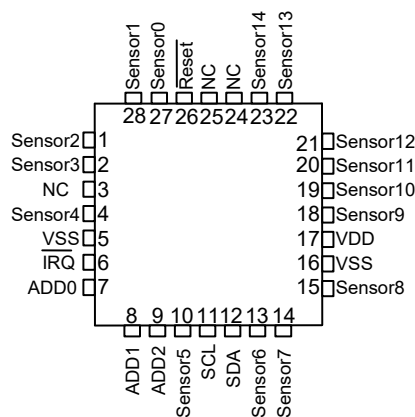
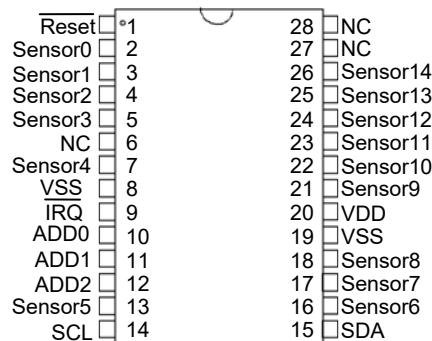
The **AlSensis**® touch sensing solution differentiates itself from capacitive sensors by measuring the actual touch event signature versus relying on comparisons of measured signals to predetermined thresholds; providing a reliable solution for your touch application. By measuring the signature of a touch event in combination with **AlSensis**® proprietary electrode geometries, potential interference from EMI, moisture and surface contaminants is eliminated. Another benefit of measuring the signature of the touch event is the ability to distinguish between a glove touch and a “bare finger” touch while giving the user the same feel of sensitivity.

Communication with the A0061 IC is provided via I²C protocol. Individual and multiple sensor scans can be requested as well as device status, and configuration.

The A0061 ASIC is very easy to integrate into products by reducing the amount up-front engineering normally required for implementing capacitive solutions which reduces your time to market and development costs.

Features

- Complete BASIC 1-15 input Touch Solution
- Patented HSS™ Touch Signature Recognition
- I²C Communication
- 20uA quiescent current (Hibernate Mode)
- Electrodes can be made from etched copper, printed silver, Indium Tin Oxide (ITO) and more.
- Electrode substrates can be PCB, Flex PCB, PET, Polyimide, Polycarbonate, and glass.
- Touch surface substrates can be glass, plastic, composites, wood, leather, fabric, and other non-conductive materials.



Ordering Information

Part Number Format: A0061 000 X Y Z



Z: Packaging Q = 28 Pin QFN, R = 28 Pin SSOP, S = 28 Pin SOIC

Y: Temperature Range 1 = -40C — +85C, 2 = -40C — 105C

X: Application A = Automotive, C = Consumer/Industrial

Electrical Characteristics

Absolute Maximum Ratings

Designation	Item	Condition	Rated Value	Unit
T _{amb}	Ambient Temperature		-40 — +125	°C
T _{stg}	Storage Temperature		-65 — +150	°C
V _{dd}	Supply Voltage	Voltage on V _{dd} with respect to V _{ss}	-0.3 — 5.5	V
I _{IRQ}	IRQ line current	Sinking	10	mA

Operating Conditions

Designation	Item	Condition	Rated Value			Unit
			Min	Nom	Max	
Supply Voltage = 5Vdc						
V _{dd}	Supply Voltage	+/-5%	4.75	5.0	5.25	V
I _{dd}	Supply Current (Run mode)		-	8.4	11.8	mA
	Supply Current (Hibernate mode)		-	22	80	µA
V _{rl}	Reset Low Voltage	0.2*V _{dd}	0.95	1.0	1.05	V
V _{rh}	Rest High Voltage	0.8*V _{dd}	3.8	4.0	4.2	V
IRQ _l	IRQ Low voltage		-	-	0.6	V
Supply Voltage = 3.3Vdc						
V _{dd}	Supply Voltage	+/-5%	3.135	3.3	3.465	V
I _{dd}	Supply Current (Run mode)		-	8.2	11.1	mA
	Supply Current (Hibernate mode)		-	20	70	µA
V _{rl}	Reset low Voltage	0.2*V _{dd}	0.627	0.660	0.693	V
V _{rh}	Rest High Voltage	0.8*V _{dd}	2.508	2.64	2.772	V
IRQ _l	IRQ Low voltage		-	-	0.6	V
Common						
T _{rst}	Reset Low pulse width Timing		2	-	-	ms
T _{startup}	Time before IC is ready for Config.	First Time power up	40	70	140	ms
	Touch Response	Run mode		40		ms
	Touch Response	From hibernate mode		70		ms

28 Pin SO Diagram

Reset	1	28	NC
Sensor0	2	27	NC
Sensor1	3	26	Sensor14
Sensor2	4	25	Sensor13
Sensor3	5	24	Sensor12
NC	6	23	Sensor11
Sensor4	7	22	Sensor10
VSS	8	21	Sensor9
IRQ	9	20	VDD
ADD0	10	19	VSS
ADD1	11	18	Sensor8
ADD2	12	17	Sensor7
Sensor5	13	16	Sensor6
SCL	14	15	SDA

Pin	Name	Function	Description / Connection
1	Reset	Input	Allows for manual reset of ASIC ⁽³⁾
2	Sensor 0	Touch Sensor Input	Connect to Sensor 0 Electrode ⁽¹⁾
3	Sensor 1	Touch Sensor Input	Connect to Sensor 1 Electrode ⁽¹⁾
4	Sensor 2	Touch Sensor Input	Connect to Sensor 2 Electrode ⁽¹⁾
5	Sensor 3	Touch Sensor Input	Connect to Sensor 3 Electrode ⁽¹⁾
6	NC		Unused pin — Do not connect
7	Sensor 4	Touch Sensor Input	Connect to Sensor 4 Electrode ⁽¹⁾
8	VSS	Power	Ground connection
9	IRQ	Open Collector Output	Data Ready signal line ⁽³⁾
10	ADD0	Digital Input	I ² C Device Address Bit 0 ⁽²⁾
11	ADD1	Digital Input	I ² C Device Address Bit 1 ⁽²⁾
12	ADD2	Digital Input	I ² C Device Address Bit 2 ⁽²⁾
13	Sensor 5	Touch Sensor Input	Connect to Sensor 5 Electrode ⁽¹⁾
14	SCL	Communication	I ² C Clock Bus

Note:

- 1 - 10kohm in series resistor required on all Sensor pins (See Application Example for Specific Details).
- These pins are used to configure the 3 least significant bits of the ASIC's device slave address for I²C communication protocol.
- See application example for specific details

28 Pin SO Diagram (cont.)

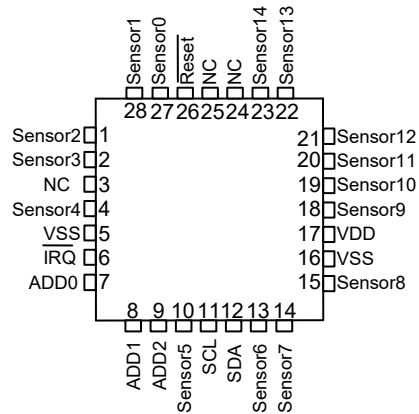
Reset	1	28	NC
Sensor0	2	27	NC
Sensor1	3	26	Sensor14
Sensor2	4	25	Sensor13
Sensor3	5	24	Sensor12
NC	6	23	Sensor11
Sensor4	7	22	Sensor10
VSS	8	21	Sensor9
IRQ	9	20	VDD
ADD0	10	19	VSS
ADD1	11	18	Sensor8
ADD2	12	17	Sensor7
Sensor5	13	16	Sensor6
SCL	14	15	SDA

Pin	Name	Function	Description / Connection
15	SDA	Communication	I ² C Data Bus
16	Sensor 6	Touch Sensor Input	Connect to Sensor 6 Electrode ⁽¹⁾
17	Sensor 7	Touch Sensor Input	Connect to Sensor 7 Electrode ⁽¹⁾
18	Sensor 8	Touch Sensor Input	Connect to Sensor 8 Electrode ⁽¹⁾
19	VSS	Power	Ground connection
20	VDD	Power	Supply Voltage
21	Sensor 9	Touch Sensor Input	Connect to Sensor 9 Electrode ⁽¹⁾
22	Sensor 10	Touch Sensor Input	Connect to Sensor 10 Electrode ⁽¹⁾
23	Sensor 11	Touch Sensor Input	Connect to Sensor 11 Electrode ⁽¹⁾
24	Sensor 12	Touch Sensor Input	Connect to Sensor 12 Electrode ⁽¹⁾
25	Sensor 13	Touch Sensor Input	Connect to Sensor 13 Electrode ⁽¹⁾
26	Sensor 14	Touch Sensor Input	Connect to Sensor 14 Electrode ⁽¹⁾
27	NC		Unused pin — Do not connect
28	NC		Unused pin — Do not connect

Note:

- 1 - 10kohm in series resistor required on all Sensor pins (See Application Example for Specific Details).
2. These pins are used to configure the 3 least significant bits of the ASIC's device slave address for I²C communication protocol.
3. See application example for specific details

28 Pin QFN Diagram

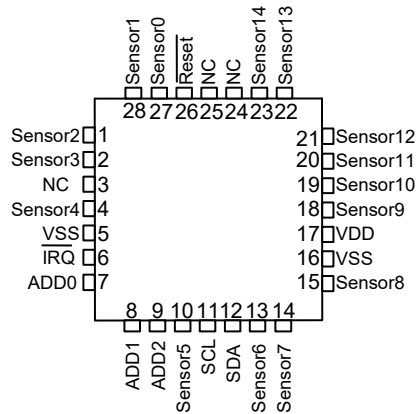


Pin	Name	Function	Description / Connection
1	Sensor 2	Touch Sensor Input	Connect to Sensor 2 Electrode ⁽¹⁾
2	Sensor 3	Communication	Connect to Sensor 3 Electrode ⁽¹⁾
3	NC		Unused pin — Do not connect
4	Sensor 4	Touch Sensor Input	Connect to Sensor 4 Electrode ⁽¹⁾
5	VSS	Power	Ground connection
6	$\overline{\text{IRQ}}$	Open Collector Output	Data Ready signal line ⁽³⁾
7	ADD0	Digital Input	I ² C Device Address Bit 0 ⁽²⁾
8	ADD1	Digital Input	I ² C Device Address Bit 1 ⁽²⁾
9	ADD2	Digital Input	I ² C Device Address Bit 2 ⁽²⁾
10	Sensor 5	Touch Sensor Input	Connect to Sensor 5 Electrode ⁽¹⁾
11	SCL	Communication	I ² C Clock Bus
12	SDA	Touch Sensor Input	I ² C Data Bus
13	Sensor 6	Touch Sensor Input	Connect to Sensor 6 Electrode ⁽¹⁾
14	Sensor 7	Touch Sensor Input	Connect to Sensor 7 Electrode ⁽¹⁾

Note:

- 1 - 10kohm in series resistor required on all Sensor pins (See Application Example for Specific Details).
- These pins are used to configure the 3 least significant bits of the ASIC's device slave address for I²C communication protocol.
- See application example for specific details

28 Pin QFN Diagram (cont.)



Pin	Name	Function	Description / Connection
15	Sensor 8	Touch Sensor Input	Connect to Sensor 8 Electrode ⁽¹⁾
16	VSS	Power	Ground connection
17	VDD	Power	Supply Voltage
18	Sensor 9	Touch Sensor Input	Connect to Sensor 9 Electrode ⁽¹⁾
19	Sensor 10	Touch Sensor Input	Connect to Sensor 10 Electrode ⁽¹⁾
20	Sensor 11	Touch Sensor Input	Connect to Sensor 11 Electrode ⁽¹⁾
21	Sensor 12	Touch Sensor Input	Connect to Sensor 12 Electrode ⁽¹⁾
22	Sensor 13	Touch Sensor Input	Connect to Sensor 13 Electrode ⁽¹⁾
23	Sensor 14	Touch Sensor Input	Connect to Sensor 14 Electrode ⁽¹⁾
24	NC		Unused pin — Do not connect
25	NC		Unused pin — Do not connect
26	Reset	Input	Allows for manual reset of ASIC ⁽³⁾
27	Sensor 0	Touch Sensor Input	Connect to Sensor 0 Electrode ⁽¹⁾
28	Sensor 1	Touch Sensor Input	Connect to Sensor 1 Electrode ⁽¹⁾

Note:

- 1 - 10kohm in series resistor required on all Sensor pins (See Application Example for Specific Details).
- 2 - These pins are used to configure the 3 least significant bits of the ASIC's device slave address for I²C communication protocol.
- 3 - See application example for specific details

Application Information

Block Diagram

Figure 1 shows a block diagram highlighting the inputs and outputs for a standard 15 input design. Typical electrodes for a touch input design can be made by creating a 14mm diameter solid copper pad on your circuit board layout. The PCB track connecting the electrode to the IC should be as thin and short as possible. Typical trace width being 10mils. (Please refer to our application notes for more information regarding electrode and substrate design.) The electrodes are connected to the A0061 through series resistors with values ranging from 1k—10k ohm. Place the resistors as close to the IC as possible. Communication to the A0061 is accomplished through a standard master slave protocol with a 7 bit address.

Set the address by connecting the ADD0—ADD2 lines to either GND or VDD to create a unique address for each IC in the system. The IRQ line serves as an interrupt to the Host micro when the IC needs to be configured or when a touch/release signature occurs.

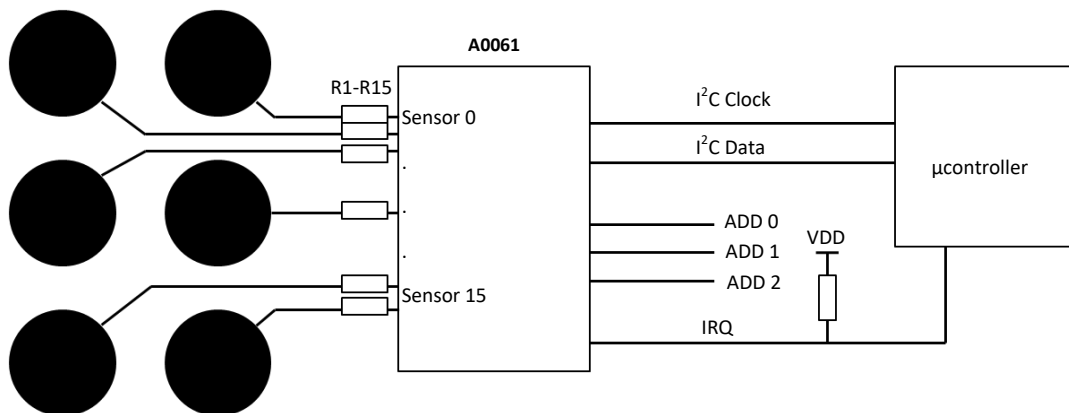


Figure 1

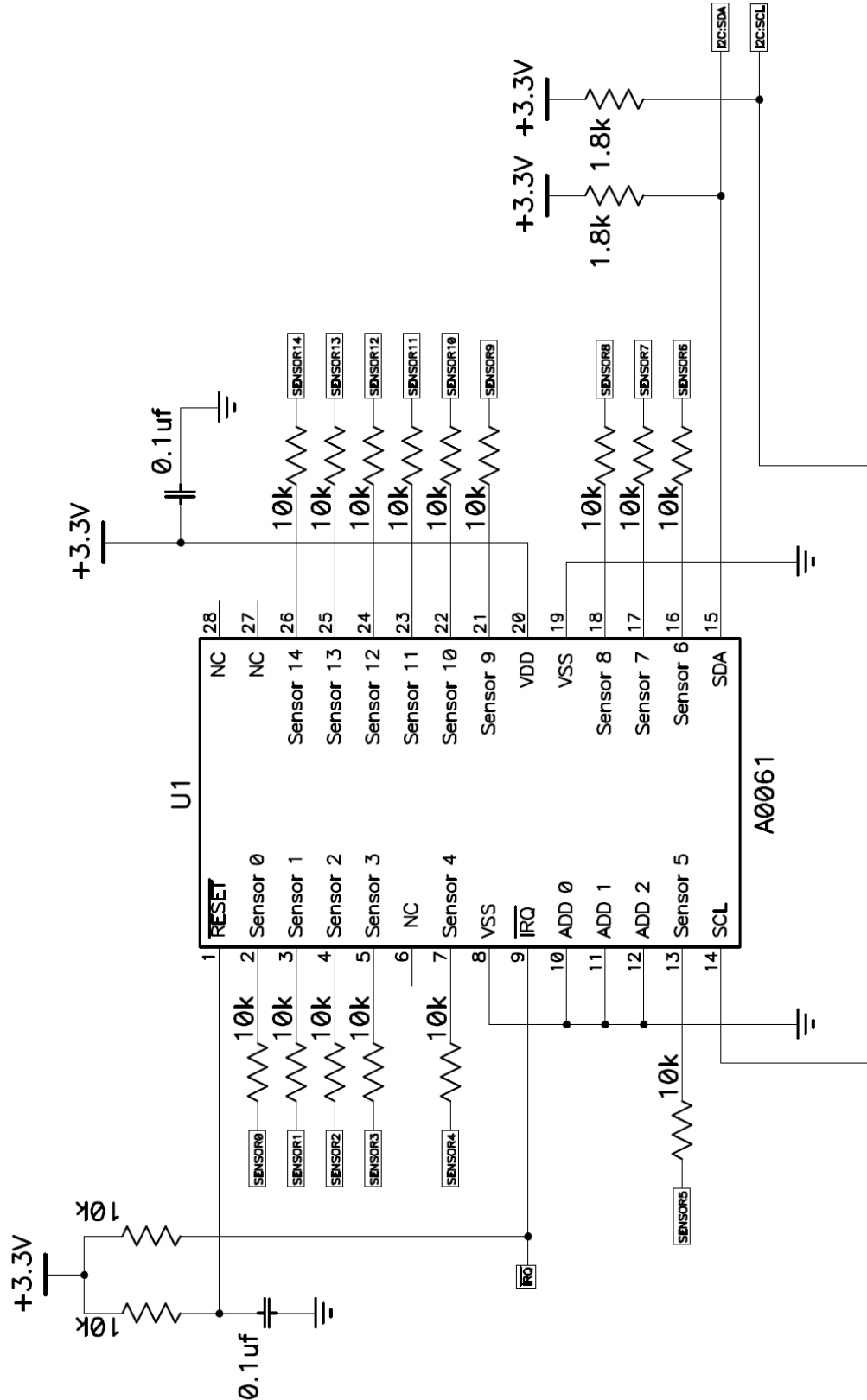
Layout Considerations

Always use engineering best practices with design and layout of your A0061 circuit. Place decoupling capacitors close to the IC. Place the RC reset circuit close to pin 4 for a clean reset on power up. Place the series sensor resistors as close to the IC as possible for input transient protection. Place the I²C pull-up resistors close to the first receiver when using more than 1 receiver on the bus. Please refer to the latest I²C specification for further clarification. The routing for the sensor electrodes should be as clean and direct as possible. Keep the ground and

power plane from routing under the sensor lines and electrodes. When on the same layer, keep the ground plane back at least 1.3mm however this dimension can change depending on system requirements.

Application Information

Reference Schematic



I²C Commands

Addressing

The A0061 communicates on the I²C protocol. Addressing for this IC is done so using a 7-Bit addressing scheme. The first byte after the START condition is the Slave address. This determines which slave IC is selected by the master. The least significant bit indicates the data direction. A 1 in the least significant bit means the master will read information from the slave. A 0 in this bit position means the master will write information to the slave. The R/W bit is shown in Figure 3. When the START condition and address is sent, each device in the system compares the upper 7 bits with its address. When a match occurs, the IC considers itself address by the master.

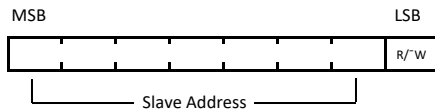


Figure 3

The upper half of the address byte is fixed to 0xBX. The address is unique to each IC then by wiring the address lines to either GND or VCC. The combination of the fixed address, address lines and the R/W bit is shown in Figure 4.

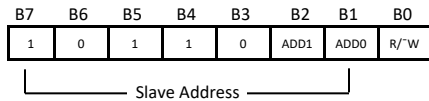


Figure 4

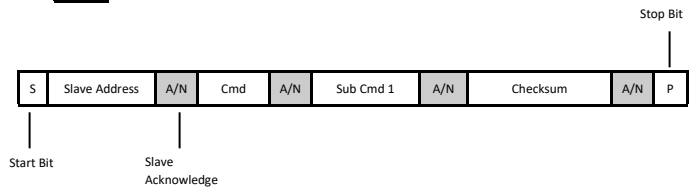
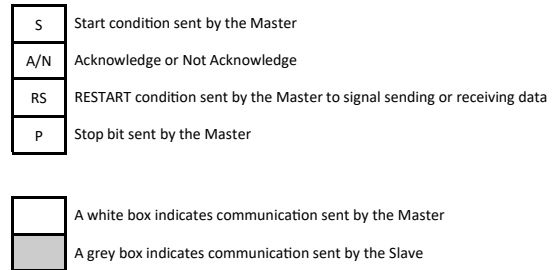
The table shown at Figure 5 demonstrates the possible address values based on the above information.

	B7	B6	B5	B4	B3	B2	B1	B0
Value	1	0	1	1	ADD2	ADD1	ADD0	R/W
0xB0	1	0	1	1	0	0	0	X
0xB2	1	0	1	1	0	0	1	X
0xB4	1	0	1	1	0	1	0	X
0xB6	1	0	1	1	0	1	1	X
0xB8	1	0	1	1	1	0	0	X
0xBA	1	0	1	1	1	0	1	X
0xBC	1	0	1	1	1	1	0	X
0xBE	1	0	1	1	1	1	1	X

Figure 5

Message Structure

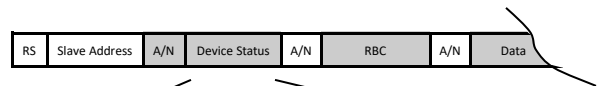
The I²C message structure for the A0061 is as follows.



The checksum can be omitted or ignored if the use of a checksum is not desired. Calculation of the checksum is done by performing an 8-bit addition of all bytes transmitted after the device address byte. If the above message was: Slave Address 0xB2; Cmd 0x0A; Sub Cmd 1 0x03, the Checksum would be : (0x0A + 0x03) = 0x0D.

Device Status Byte

For any message that has return data from the Slave, the Master sends a RESTART followed by the Slave address. The Slave will send an acknowledge followed by a Device Status byte. The definition of the device status bits are shown below. Data after the Device Status byte depends on the command. Please see the I²C commands.



Device Status Byte							
Error Code				Device State			
B7	B6	B5	B4	B3	B2	B1	B0
Error Code: < B7 : B4 >				Device State: < B3 : B0 >			
0 : No Error				1 : Ready and monitoring sensors			
1 : Checksum mismatch				2 : Waiting for configuration			
2 : Unknown command				3 : Configuring			
3 : Command format error				4 : Initializing			
4 : Command not allowed				5 : Hibernating			
5 : DOT error				6 : Diagnostic			

I²C: Write Configuration 0x0A

S	Slave Address	A/N	Cmd	A/N	Sub Cmd 1	A/N	Sub Cmd 2	A/N	Checksum	A/N	P
---	---------------	-----	-----	-----	-----------	-----	-----------	-----	----------	-----	---

Byte	Contents	Limits	Description
Cmd	0x0A		Command for declaring the number of sensors to be used.
Sub Cmd 1	Sensor Enable Mask 1	0x00	The Sensor Enable Mask is a bitwise enable of each sensor. A value of 0 will disable a sensor. A value of 1 will enable the sensor.
		0xFF	The value of 0xFF will enable sensors 0 – 7.
Sub Cmd 2	Sensor Enable Mask 2	0x00	The value of 0x00 will disable sensors 8 – 14
		0x7F	The value of 0x7F will enable sensors 8 – 14

After the IC has been reset through either the hardware reset or I²C reset message, it will be in the “Waiting For Configuration” state and the IRQ line will be low. At this time the IC will not monitor any electrodes. After the Set TRZ Command, the next message that should be sent is the Write Configuration command. This command combined with the sensor mask will enable any or all sensors by setting its corresponding bit position to a high level. After the IC has been initialized. It will change to the “Configuring” state before quickly changing to the “Initializing” state and finally to the “Ready” state within 50-55ms. At this time the IC will monitor only the sensors that are enabled. All other sensors will read as “not touched”.

I²C: Read Configuration 0x8A

S	Slave Address	A/N	Cmd	A/N	Checksum	A/N	RS	Slave Address	A/N	Device Status	A/N	RBC	A/N	Data	A/N	Checksum	A/N	P
---	---------------	-----	-----	-----	----------	-----	----	---------------	-----	---------------	-----	-----	-----	------	-----	----------	-----	---

Byte	Contents	Limits	Description
Cmd	0x8A		Command for reading the configuration of the sensors. Either 0 for disabled or 1 for enabled.
Device Status			See the description on Page 9.
RBC	Return Data Byte Count	2	RBC is the number of data bytes that the device will return. This device will always return a 1 in RBC for this message.
Data	Bitwise value of the sensor configura-	0x00 : 0x00 - 0xFF: 0x7F	0 - disabled 1 - Enabled

The Read Configuration command reports the status of which sensors are enabled. After the Master sends the command, an RS is issued and the Slave will report the Device Status, A return byte of 2, and the bitwise condition of each sensor. The bit will be 0 if the sensor is disabled, and a 1 if the sensor is enabled. See the table below for further explanation.

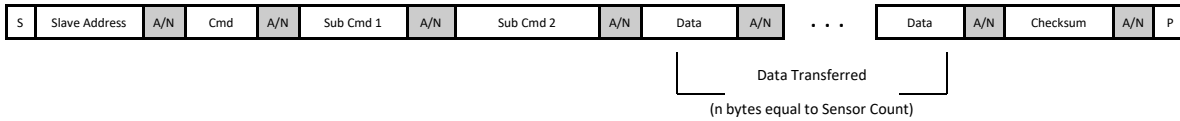
Sensor Enable Mask

For the Write and Read Configuration commands, the sensors are enabled or disabled by these bitwise locations. A value of 0x11 in the mask 1 means that Sensor 4 and 0 are enabled. B7 of sensor mask 2 is ignored by the slave.

	Sensor Enable Mask 1							
	B7	B6	B5	B4	B3	B2	B1	B0
Sensor	Sensor 7	Sensor 6	Sensor 5	Sensor 4	Sensor 3	Sensor 2	Sensor 1	Sensor 0

	Sensor Enable Mask 2							
	B7	B6	B5	B4	B3	B2	B1	B0
Sensor	NA	Sensor 14	Sensor 13	Sensor 12	Sensor 11	Sensor 10	Sensor 9	Sensor 8

I²C: Set Sensor TRZ 0x0B



Byte	Contents	Limits	Description
Cmd	0x0B		Command for setting the TRZ value for 1 or more sensors
Sub Cmd 1	Sensor Count	1 - 15	Holds the number of sensors that will receive a new TRZ value
Sub Cmd 2	Start Sensor #	0 - 14	Holds the starting sensor number to be initialized
		0xFF	By placing a 0xFF in the start sensor #, all sensors will be set with the same TRZ value located in the first
Data[0] , Data[1]...	TRZ Value	0x00 - 0x3F	The TRZ value is sent consecutively for each sensor

The Outer Zone is the distance from the electrode dictating when the IC starts to monitor the electric field variations for a touch signature event. The larger the Outer Zone number, the further away from the electrode HSS™ will start to look for touch signatures. This is useful when using HSS™ to sense through thicker plastics. The Outer Zone number is in the upper nibble of Data Byte. Shown below.

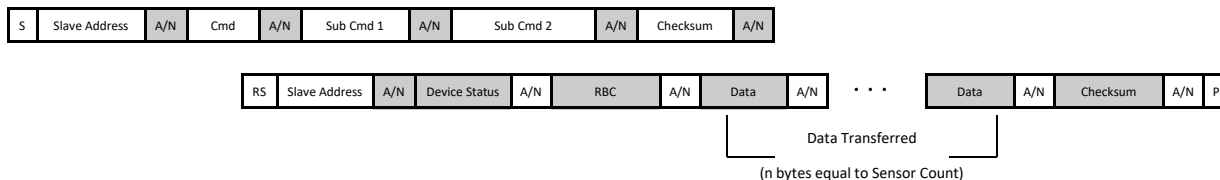
	Outer Zone				Inner Zone			
	b7	b6	b5	b4	b3	b2	b1	b0
Higher Outer Zone ↑	x	x	x	x				
	x	x	x	x				
	0	0	1	1				
	0	0	1	0				
	0	0	0	1				
	0	0	0	0				
	0	0	0	0				

Outer Zone				Inner Zone				Higher Inner Zone ↑
b7	b6	b5	b4	b3	b2	b1	b0	
				1	1	1	1	
				1	1	1	0	
				0	0	0	1	
				0	0	0	0	

The TRZ value can be set for each individual sensor by setting the sensor count to 1 and the start sensor to the specific sensor of interest. Alternately, by setting the start sensor number to 0xFF, all sensors can be set to the same TRZ value. This is useful to limit the communication time if all sensors have the same TRZ however in most practical designs, the TRZ will differ from electrode to electrode based on factors such as parasitic capacitances from PCB traces to each electrode.

Changing the Inner Zone value will set the area above the sensor that the Touch Signature event must end within. These 2 parameters are used in conjunction. When a finger approaches an electrode, HSS™ starts to evaluate it for a Touch Signature after the finger enters the Outer Zone. If the Finger continues through the Outer Zone and into the Inner Zone and completes a Touch Signature inside of the Inner Zone, HSS™ will report that a valid touch has occurred. Combining these 2 parameters results achieving a consistent touch performance through even thick plastics or glass.

I²C: Read Sensor TRZ 0x8B



Byte	Contents	Limits	Description
Cmd	0x8B		Command for reading the TRZ value for 1 or more sensors.
Sub Cmd 1	Sensor Count	1 - 15	Holds the number of sensors that will return their TRZ value to the Master.
Sub Cmd 2	Start Sensor #	0 - 14	Holds the starting sensor number to be read. (If 0xFF is in Sensor Count, Start Sensor# is ignored)
Device Status			See the description on Page 9.
RBC	Return Byte Count	1-15	Each TRZ is 1 byte in length. The return byte count will be the same as sensor count.
Data[0] , Data[1]...	TRZ Value	0x00 - 0x3F	The TRZ value is sent consecutively for each sensor.

The Read TRZ command allows for verification of the TRZ values previously set. The protocol for reading individual or sensors is the same for the Read command as it is for the Write command.

I²C: Write Latch-up Timeout 0x0C

S	Slave Address	A/N	Cmd	A/N	Sub Cmd 1	A/N	Checksum	A/N	P
---	---------------	-----	-----	-----	-----------	-----	----------	-----	---

Byte	Contents	Limits	Description
Cmd	0x0C		Command for setting the time to wait before releasing button that has been in the Touch State.
Sub Cmd 1	Timeout Value	0x00	A value of 0x00 will turn off the Latch-up timeout feature.
		0xFF	The value of 0x01—0xFF will set the Latch-up timeout value from 1 to 255 seconds. The Default value is set to 0xFA (250 seconds).

The Latch-up timeout command establishes the duration of time that the IC must wait before considering that a continuous button press is latched on for any reason. When this time expires, the IC will pull the IRQ line low to signal a change in data to button off and re-calibrate the No-Touch State of the button. Sending a 0x00 in Sub Cmd 1 location will turn off the Latch-up timer so that a re-calibration will never occur. A 0xFF results in 255 seconds. If the command is sent while the button is pressed, and the timeout value is greater than the time the button has been pressed, the button will be released at the end of the timeout. If the command is sent while the button is pressed but the timeout value is less than the time the button has been currently pressed, the button will be released immediately.

I²C: Read Latch-up Timeout 0x8C

S	Slave Address	A/N	Cmd	A/N	Checksum	A/N	RS	Slave Address	A/N	Device Status	A/N	RBC	A/N	Data	A/N	Checksum	A/N	P
---	---------------	-----	-----	-----	----------	-----	----	---------------	-----	---------------	-----	-----	-----	------	-----	----------	-----	---

Byte	Contents	Limits	Description
Cmd	0x8C		Command for reading the Latch-up timeout value.
Device Status			See the description on Page 9.
RBC	Return Data Byte Count	1	RBC is the number of data bytes that the device will return. This device will always return a 1 in RBC for this message.
Data	Timeout Value	0x00 - 0xFF	The value of 0x01—0xFF will the Latch-up timeout value from 1 to 255 seconds.

The Read Latch-up timeout command reports the time duration stored from the Write Command. After the Master sends the Command, an RS is issued and the Slave will report the Device Status, a return byte count of 1 and the Latch-up Timeout value.

I²C: Write BounceBack Timeout 0x0D

S	Slave Address	A/N	Cmd	A/N	Sub Cmd 1	A/N	Checksum	A/N	P
---	---------------	-----	-----	-----	-----------	-----	----------	-----	---

Byte	Contents	Limits	Description
Cmd	0x0D		Sets the time to allow a fast adjust to a No Touch after an initialization.
Sub Cmd 1	Timeout Value	0x00	A value of 0x00 turns the BounceBack Timer off so that adjustments are made at the normal rate.
		0x01 - 0x1E	The value of 0x01—0x1E (30dec) will set the BounceBack timeout value from 1 to 30 seconds. The Default value is set to 0x1E (30seconds).

The BounceBack Timeout is used to quickly adjust the No Touch calibration state after power up, reset, or sleep mode. The scenario may be that a touch is present at power up or before the IC wakes from sleep. During these conditions a No Touch calibration occurs. If a touch is present during this time it will be seen as a No Touch Condition. If BounceBack is enabled and the touch is removed within the BounceBack Timeout period, up to 30 seconds, HSS™ will quickly re-adjust the No Touch state and begin to recognize new touch signatures immediately. If BounceBack is not enabled, and the above condition occurs, new touch signatures may not be recognized for up to 30 seconds.

I²C: Read BounceBack Timeout 0x8D

S	Slave Address	A/N	Cmd	A/N	Checksum	A/N	RS	Slave Address	A/N	Device Status	A/N	RBC	A/N	Data	A/N	Checksum	A/N	P
---	---------------	-----	-----	-----	----------	-----	----	---------------	-----	---------------	-----	-----	-----	------	-----	----------	-----	---

Byte	Contents	Limits	Description
Cmd	0x8D		Command for reading the BounceBack Timeout value.
Device Status			See the description on Page 9.
RBC	Return Data Byte Count	1	RBC is the number of data bytes that the device will return. This device will always return a 1 in RBC for this message.
Data	Timeout Value	0x00—0x1E	The stored BounceBack timeout value will be from 0x00 (off) - 0x1E (30seconds).

The Read BounceBack timeout command reports the time duration stored from the Write Command. After the Master sends the Command, an RS is issued and the Slave will report the Device Status, a return byte count of 1 and the BounceBack Timeout value.

I²C: Read Sensor States 0x9F

S	Slave Address	A/N	Cmd	A/N	Checksum	A/N	RS	Slave Address	A/N	Device Status	A/N	RBC	A/N	Data	A/N	Checksum	A/N	P
---	---------------	-----	-----	-----	----------	-----	----	---------------	-----	---------------	-----	-----	-----	------	-----	----------	-----	---

Byte	Contents	Limits	Description
Cmd	0x9F		Command for reading the state of all sensors
Device Status			See the description on Page 9
RBC	Return Data Byte Count	2	RBC is the number of data bytes that the device will return. This device will always return a 2 in RBC for this message.
Data	Bitwise value of the sensors	0x00 - 0x3F	0 - Not Touched 1 - Touched

The Read Sensor States command is used to read the bitwise touch value of the sensors. When no sensor is touched, the data bytes 1 and 2 will read 0x00. When sensor 2 is touched, the data byte 1 will read 0x04 and data byte 2 will read 0x00.

	Data Byte 1							
	B7	B6	B5	B4	B3	B2	B1	B0
Sensor	Sensor 7	Sensor 6	Sensor 5	Sensor 4	Sensor 3	Sensor 2	Sensor 1	Sensor 0

	Data Byte 2							
	B7	B6	B5	B4	B3	B2	B1	B0
Sensor	NA	Sensor 14	Sensor 13	Sensor 12	Sensor 11	Sensor 10	Sensor 9	Sensor 8

I²C: Read Device Status 0x9E

S	Slave Address	A/N	Cmd	A/N	Checksum	A/N	RS	Slave Address	A/N	Device Status	A/N	RBC	A/N	Data	A/N	Checksum	A/N	P
---	---------------	-----	-----	-----	----------	-----	----	---------------	-----	---------------	-----	-----	-----	------	-----	----------	-----	---

Byte	Contents	Limits	Description
Cmd	0x9E		Command for reading the Device Status
Device Status			See the description on Page 9
RBC	Return Data Byte Count	1	RBC is the number of data bytes that the device will return. This device will always return a 1 in RBC for this message.
Data	Extended Status	0x00 - 0xFF	Equal to the command that caused the error.

The Read Device Status command is used to determine what state the IC is in and if there are any error codes. These are defined on page 6. If there is an error code present, the I²C command that caused the error will be placed the Extended Status byte.

I²C: Clear Device Errors 0x1E

S	Slave Address	A/N	Cmd	A/N	Sub Cmd 1	A/N	Checksum	A/N	P
---	---------------	-----	-----	-----	-----------	-----	----------	-----	---

Byte	Contents	Limits	Description
Cmd	0x1E		Command for Clearing any Device Errors
Sub Cmd 1	Clear Errors	0xFF	The Clear Error byte must contain a 0xFF for the device to clear errors.

Sending the Clear Device Errors command with a 0xFF will clear the upper nibble of the Device Status byte.

I²C: Set Hibernate State 0x14

S	Slave Address	A/N	Cmd	A/N	Sub Cmd 1	A/N	Checksum	A/N	P
---	---------------	-----	-----	-----	-----------	-----	----------	-----	---

Byte	Contents	Limits	Description
Cmd	0x14		Command for setting the device to hibernate or awake
Sub Cmd 1	State	0x00	A 0 in the state byte sets the IC to the awake mode
		0x01	A 1 in the state byte sets the IC to the hibernate mode

The Hibernate command is used to place the device in a low power consumption hibernate state. This command can also set the device to resume normal operation. Sending this command with a 0x01 in the Sub Cmd 1 location will cause the IC to stop monitoring all sensors and go into a low power hibernate mode. During this time, all sensors will return the “not touched” condition. To resume normal operation and scan all active sensors, simply send the same command with a 0x00 in the Sub Cmd 1. When the IC receives the wake command, it will change to the “initializing device” state for 40ms before changing to the “ready” state.

I²C: Reset 0x52

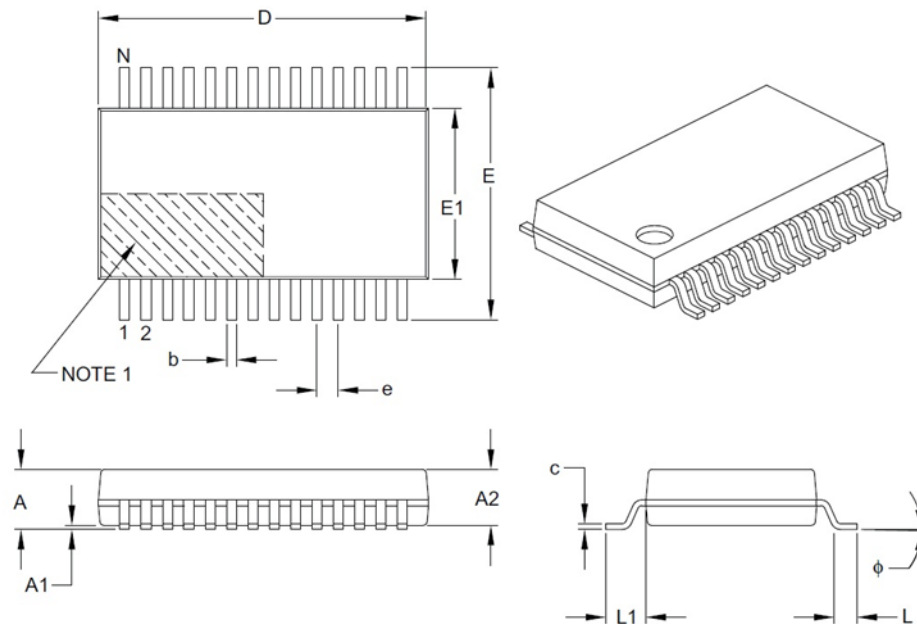
S	Slave Address	A/N	Cmd	A/N	Sub Cmd 1	A/N	Checksum	A/N	P
---	---------------	-----	-----	-----	-----------	-----	----------	-----	---

Byte	Contents	Limits	Description
Cmd	0x52		Command for resetting the device
Sub Cmd 1	Reset	0x00	A 0 in the Reset byte does nothing
		0x01	A 1 in the Reset byte causes the device to reset and waits in the “Waiting for Configuration” state

Sending the Reset command with a 0x01 will clear out all configuration including TRZ values. The IC will stop monitoring sensors and will be in the “Waiting For Configuration” state.

SSOP Package Details

Typical 28-Lead Plastic Shrink Small Outline – 5.30 mm Body [SSOP]



Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Number of Pins	N	28		
Pitch	e	0.65 BSC		
Overall Height	A	–	–	2.00
Molded Package Thickness	A2	1.65	1.75	1.85
Standoff	A1	0.05	–	–
Overall Width	E	7.40	7.80	8.20
Molded Package Width	E1	5.00	5.30	5.60
Overall Length	D	9.90	10.20	10.50
Foot Length	L	0.55	0.75	0.95
Footprint	L1	1.25 REF		
Lead Thickness	c	0.09	–	0.25
Foot Angle	φ	0°	4°	8°
Lead Width	b	0.22	–	0.38

Notes:

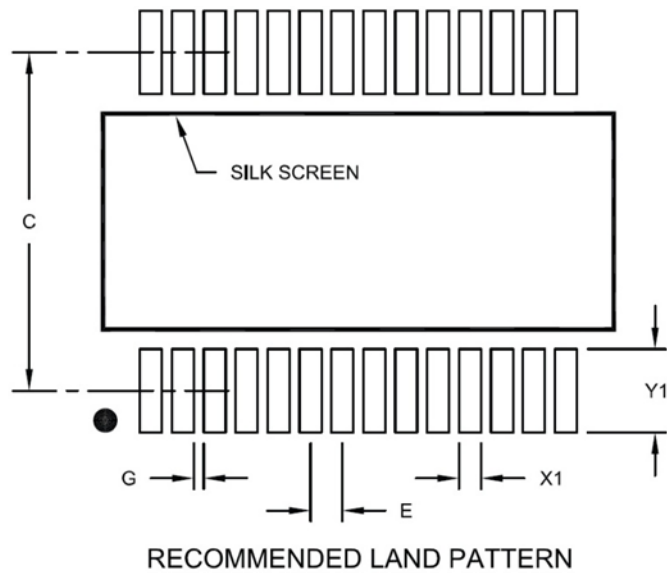
- Pin 1 visual index feature may vary, but must be located within the hatched area.
- Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.20 mm per side.
- Dimensioning and tolerancing per ASME Y14.5M.

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

REF: Reference Dimension, usually without tolerance, for information purposes only.

SSOP Recommended Land Pattern

Typical 28-Lead Plastic Shrink Small Outline – 5.30 mm Body [SSOP]



Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Contact Pitch	E		0.65 BSC	
Contact Pad Spacing	C		7.20	
Contact Pad Width (X28)	X1			0.45
Contact Pad Length (X28)	Y1			1.75
Distance Between Pads	G	0.20		

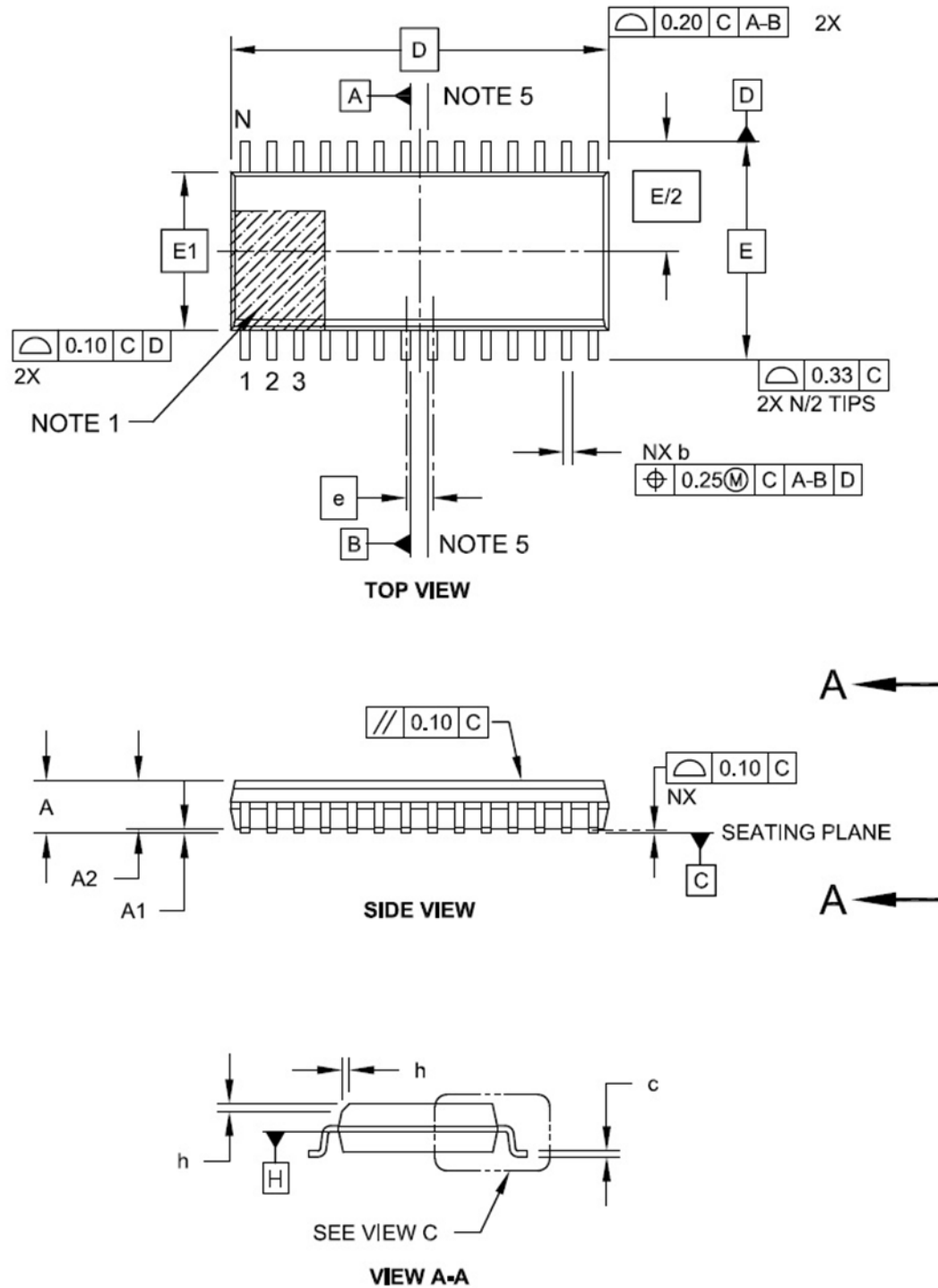
Notes:

1. Dimensioning and tolerancing per ASME Y14.5M

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

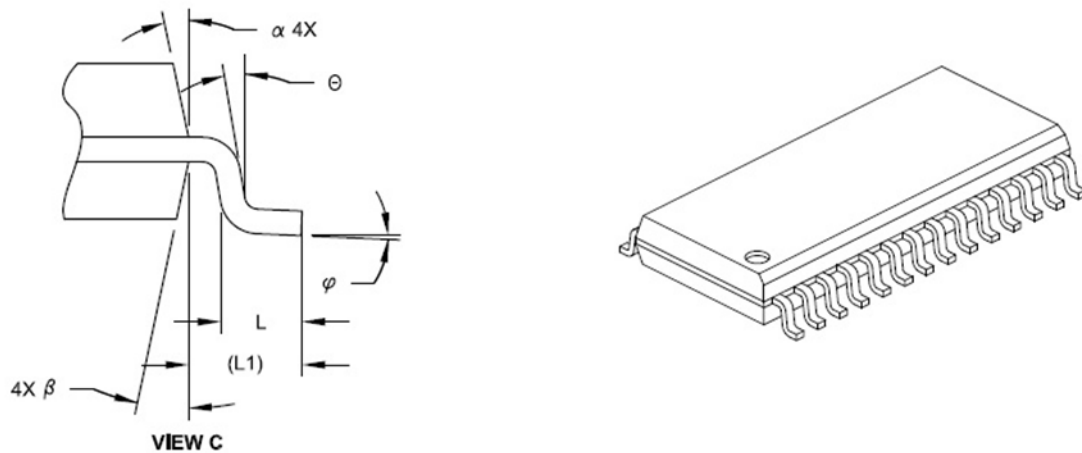
SOIC Package Details

Typical 28-Lead Plastic Small Outline – Wide, 7.50 mm Body [SOIC]



SOIC Package Details

Typical 28-Lead Plastic Small Outline – Wide, 7.50 mm Body [SOIC]



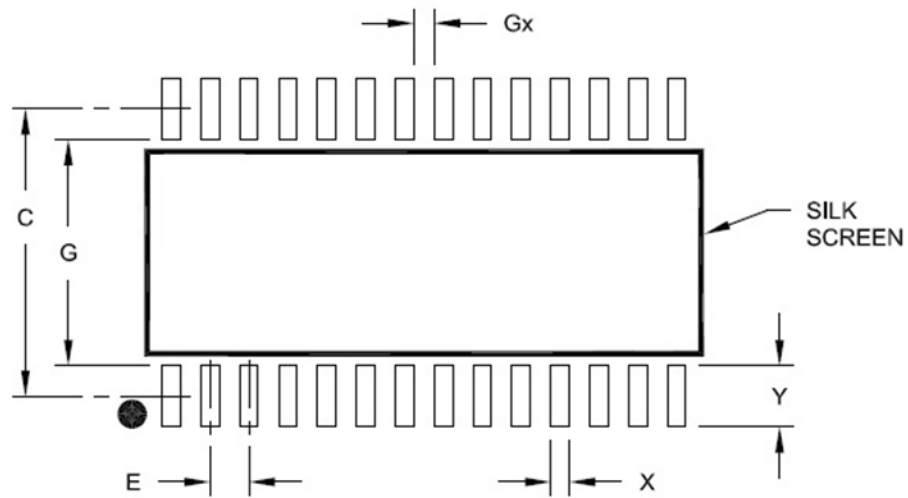
Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Number of Pins	N	28		
Pitch	e	1.27 BSC		
Overall Height	A	-	-	2.65
Molded Package Thickness	A2	2.05	-	-
Standoff §	A1	0.10	-	0.30
Overall Width	E	10.30 BSC		
Molded Package Width	E1	7.50 BSC		
Overall Length	D	17.90 BSC		
Chamfer (Optional)	h	0.25	-	0.75
Foot Length	L	0.40	-	1.27
Footprint	L1	1.40 REF		
Lead Angle	Θ	0°	-	-
Foot Angle	φ	0°	-	8°
Lead Thickness	c	0.18	-	0.33
Lead Width	b	0.31	-	0.51
Mold Draft Angle Top	α	5°	-	15°
Mold Draft Angle Bottom	β	5°	-	15°

Notes:

- Pin 1 visual index feature may vary, but must be located within the hatched area.
- § Significant Characteristic
- Dimension D does not include mold flash, protrusions or gate burrs, which shall not exceed 0.15 mm per end. Dimension E1 does not include interlead flash or protrusion, which shall not exceed 0.25 mm per side.
- Dimensioning and tolerancing per ASME Y14.5M
BSC: Basic Dimension. Theoretically exact value shown without tolerances.
REF: Reference Dimension, usually without tolerance, for information purposes only.
- Datums A & B to be determined at Datum H.

SOIC Recommended Land Pattern

Typical 28-Lead Plastic Small Outline – Wide, 7.50 mm Body [SOIC]



RECOMMENDED LAND PATTERN

Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Contact Pitch	E	1.27 BSC		
Contact Pad Spacing	C		9.40	
Contact Pad Width (X28)	X			0.60
Contact Pad Length (X28)	Y			2.00
Distance Between Pads	Gx	0.67		
Distance Between Pads	G	7.40		

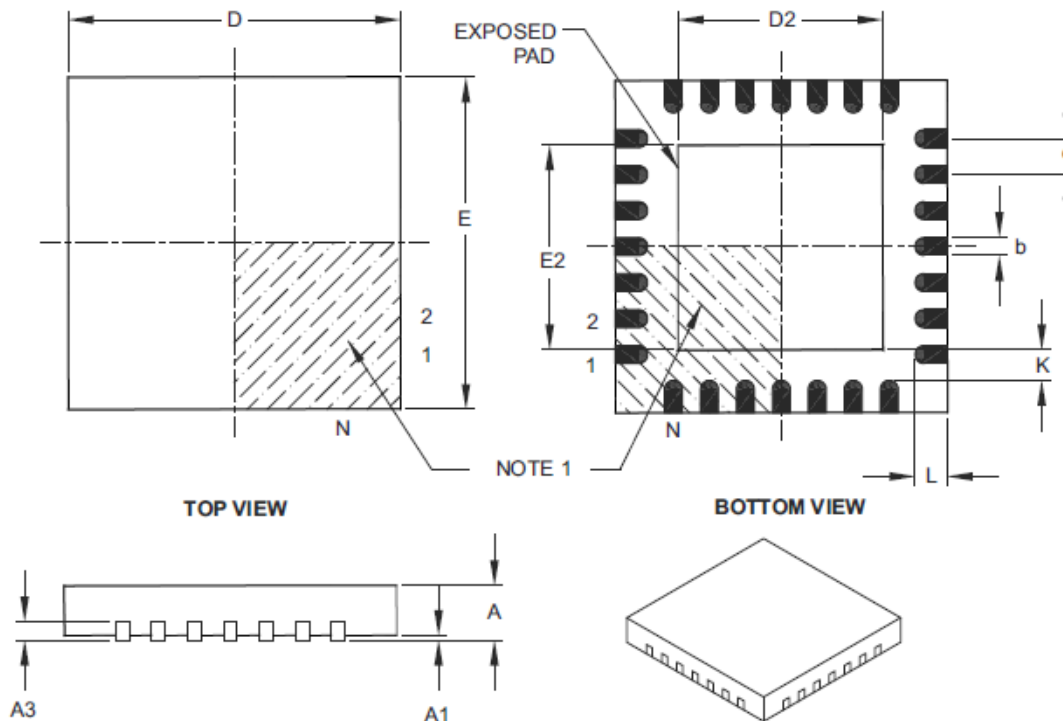
Notes:

1. Dimensioning and tolerancing per ASME Y14.5M

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

QFN Package Details

Typical 28-Lead Plastic Quad Flat, No Lead Package (ML) - 6x6 mm Body [QFN]



Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Number of Pins	N	28		
Pitch	e	0.65 BSC		
Overall Height	A	0.80	0.90	1.00
Standoff	A1	0.00	0.02	0.05
Contact Thickness	A3	0.20 REF		
Overall Width	E	6.00 BSC		
Exposed Pad Width	E2	3.65	3.70	4.20
Overall Length	D	6.00 BSC		
Exposed Pad Length	D2	3.65	3.70	4.20
Contact Width	b	0.23	0.30	0.35
Contact Length	L	0.50	0.55	0.70
Contact-to-Exposed Pad	K	0.20	-	-

Notes:

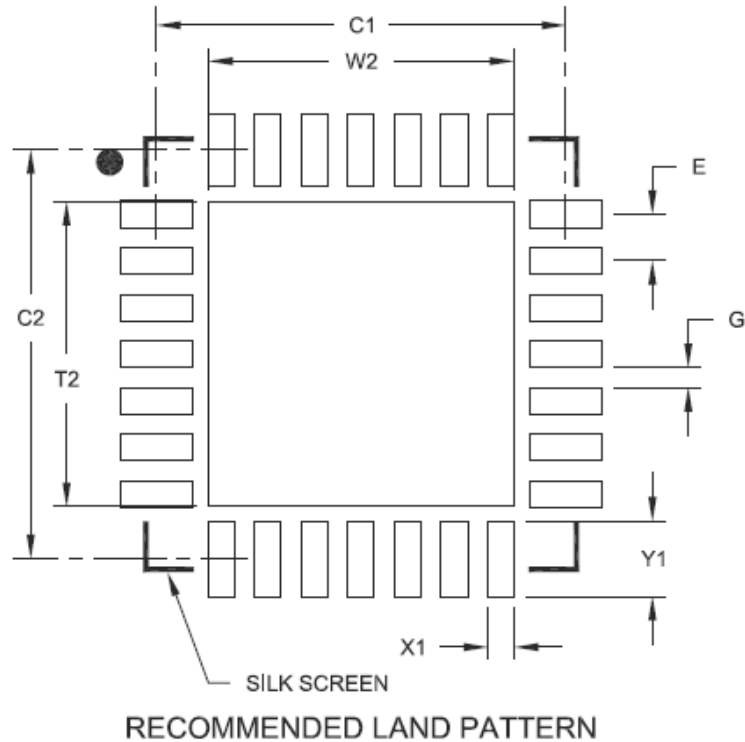
- Pin 1 visual index feature may vary, but must be located within the hatched area.
- Package is saw singulated.
- Dimensioning and tolerancing per ASME Y14.5M.

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

REF: Reference Dimension, usually without tolerance, for information purposes only.

QFN Recommended Land Pattern

Typical 28-Lead Plastic Quad Flat, No Lead Package (ML) - 6x6 mm Body [QFN]



Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Contact Pitch	E		0.65 BSC	
Optional Center Pad Width	W2			4.25
Optional Center Pad Length	T2			4.25
Contact Pad Spacing	C1		5.70	
Contact Pad Spacing	C2		5.70	
Contact Pad Width (X28)	X1			0.37
Contact Pad Length (X28)	Y1			1.00
Distance Between Pads	G	0.20		

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M

BSC: Basic Dimension. Theoretically exact value shown without tolerances.